

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Takeshi SHIOGA, et al.

Application No.: 10/629,761

Group Art Unit: 2812

Confirmation No.: 7698

Filed: July 30, 2003

Examiner: TSAI, H JEY

For: CAPACITOR AND SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SEMICONDUCTOR DEVICE

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed December 12, 2003, applicants elect Claims 13, 16-34 of Group I, without traverse.

Respectfully submitted,

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Date: <u>Jan 0, 2903</u>

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